In the United States Patent and Trademark Office

In re the application o

Lee D. Whetsel

Serial No.: 09/955,542

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Art Unit: 2133 AUG 0 9 2004

Examiner:

J. Kerveros Technology Center 2100

Title: Low Power Scan & Delay Test Method and Apparatus

AUG 0 3 2004 8

.July 29, 2004

Asst. Commissioner for Patents

•P.O. Box 1450

·Alexandria, VA 22313

Dear Sir:

ERTIFICATE UNDER 37 C.F.R. \$1.8(A) reby certify that the above correspondence is deposited with the U.S. Postal Service as First Mail in an envelope addressed to: Assistant ssioner for Patents, P.O. Box 1450, Alexandria 2313-1450 on July 29, 2004.

Transmitted herewith is an amendment in this application.

The fee has been calculated as shown below.

•		CI	LAIMS AS AMENDI	ED		
•	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
Total Claims	7	Minus	20	= 0	x \$18 =	\$ 0
Ind. Claims	1	Minus	4	= 0	x \$86 =	\$ 0
			TOTA	L ADDITIONA TH	AL FEE FOR	\$ 0

Under 37 C.F.R. § 1.16(k) please charge the total additional fee, and any further fees, or credit overpayment to Deposit) Account No. 20-0668, of Texas Instruments Incorporated.

pectfully submitted

Reg. No. 29,043

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